Module (Course Syllabus) Catalogue

2023-2024

College/ Institute	**Erbil Technology College**	
Department	**Department of AIT**	
Module Name	**Power Electronics**	
Module Code	**POE303**	
Semester	**3**	
Credits	**6**	
Module type	**Core**	
Weekly hours		
Weekly hours (Theory)	**(2)hr Class**	**(86)hr Workload**
Weekly hours (Practical)	**(2)hr Class**	**(64)hr Workload**
Lecturer (Theory)	**Farah A. Abed**	
E-Mail & Mobile NO.	**Farah.abed@epu.edu.iq 07503527165**	
Lecturer (Practical)	**Farah A. Abed**	
E-Mail & Mobile NO.	**Farah.abed@epu.edu.iq 07503527165**	

Course Book

Course Description	This is the first course in Power Electronics. Students will learn about specific areas of application and the reasons Power Electronics is becoming popular in areas traditionally occupied by analog electronics. The course will cover applications in conversion and control of power using Power semiconductor devices, physics of their structure and operation and passive components in power circuits. Students will also learn the principles governing the operation of converters, different standard topologies, applications in power systems, motor drives, and applications in renewable energy sources.
Course objectives	The course is prepared for final class students. It's highlighting on Power Electronic devices, its characteristics. Important circuit component design and analysis concepts. Controlled and uncontrolled phase rectifier circuits. Types of transistors and their applications. DC to DC converter. DC to AC inverter. IC circuits. Operational Amplifiers and their applications. Motor drives and Power electronic applications with renewable energy conversions.
Student's obligation	**Respect** A student has an obligation to exhibit honesty and to respect the ethical standards of the profession in carrying out his/her academic assignments. Without limiting the application of this principle. **Attendance** Missed classes will not be compensated including the quizzes and the scheduled assignments. The students will lose marks on unattended classes with quizzes unless a legal document or authorized leave is presented which should explain the excuse of the absence. However, the absent student should take the responsibility for making up the missed lecture. **The lectures format** The lectures are divided on four weekly hours (two hours theoretically and two hours practically). Mainly, the first two theoretical hours will be dedicated for the topic backgrounds and the main principles. Notes and handouts are given to the students containing the detail of the theoretical topics. Theoretical lectures will be assisted by presentations using white board and data show. Discussion time is provided for the students for questions. The first practical hour will be dedicated for how to works in the laboratory, health and safety, how to use equipment, boards,

	components, and wire connection to do experiments and reports. Students should submit every week a report about the previous experiment. **Questions** Asking questions about unclear material is an important part of the classroom experience. It is not uncommon for students to have similar difficulties, so speaking up will help everyone understand the discussed information. Teachers can also benefit from a student's questions. By finding out what subjects are hard to understand, instructors can adjust their lectures to clear up confusing topics. **Assignment** A student must submit the assignment on **moodle** app. every week and also write a report about what he/she was studied in the laboratory.
Assessment scheme	25% Mid Term (Theory and practical) 40% Assignment (report, paper, homework, seminar...) 20% final practical 15% final theory
Specific learning outcome:	After taking this course, students have ability to: 1. Understand the need, use and limitations of Power Electronics 2. Appreciate the linkage of Power Electronics with electromagnetics, circuits, devices, electronics, feedback, control, power systems, machines and emerging application areas like renewables, smart grids and high frequency applications. 3. Understand the factors affecting choice of devices in power electronics Lahore University of Management Sciences.
Course References:	1. Thomas L. Floyd, Electronic devices, sixth edition, Copyright @ 2004, 2002, 1999, 1996. 1992, 1988, 1984 by Pearson Education, Inc., Upper Saddle River, New Jersey Pearson Prentice Hall. 2. S. CHAND, R.S.SEDHA,A Textbook of Electronic circuits 2007. 3. Denis Fewson, Introduction to power electronics , Printed and bound in Great Britain by J W Arrow smith Ltd, Bristol , 1998. 4. Muhammad H. Rashid, SPICE for Power Electronics and Electric Power, Second Edition, Published in 2006 by CRC Press.Handbook-Six Volume Set, CRC press.

Course topics (Theory)	**Week**	**Learning Outcome**
(Power electronics) Definition and key characteristics, Semiconductor devices in power electronics, Power electronic applications.	1	

Rectification: (Single phase half wave rectifier with resistive load, Single phase full wave rectifier with resistive load)	2	
Three phase star rectifiers, Three phase bridge rectifiers. Filtering system in rectifier circuits.	3	
Bipolar junction transistors (BJT), Reviewing (Transistor structure, Basic transistor operation, Relative comparison of amplifier configuration), BJT as switch	4	
Transistor switching – performance, Improvement of transistor switching time, Main features of BJT)..	5	
Field effect transistor (MOSFET), Symbol & output characteristics, Main features of MOSFET.	6	
Insulated gate bipolar transistor (IGBT), Symbol & output characteristics, and Main features of IGBT.	7	
Uni-junction transistor (UJT), Basic construction, Equivalent Circuit, A UJT Application.	8	
OPERATIONAL AMPLIFIERS, Typical packages, ideal op-amp - practical op-amp, Input Signal Modes	9	
Negative Feedback, Op-Amp Gains	10	
APPLICATIONS OF OPERATIONAL AMPLIFIERS, Inverting Amplifier, noninverting Amplifier.by using the wattmeter, the connection for calculating active power, reactive power and apparent power.	11	
Inverting Summing Amplifier, noninverting Summing Amplifier, Integrators, Differentiator, Zero-level Detection, Nonzero-Level Detection.	12	
Practical Topics	**Week**	**Learning Outcome**
Single phase half-wave rectifier resistive load. Single phase full-wave bridge rectifier resistive load	1	
Three phase half-wave rectifier resistive load. Three phase full wave bridge rectifier.	2	
Transistor BJT as a switch.	3	

FET/MOSFET characteristics	4	
MOSFET speed control.	5	
IGBT characteristics.	6	
IGBT speed control.	7	
UJT characteristics.	8	
Operational Amplifier (OP-Amplifier) applications as inverting amplifier	9	
Operational Amplifier (OP-Amplifier) applications as non-inverting amplifier.	10	
Operational Amplifier (OP-Amplifier) applications as multivibrator amplifier.	11	
Operational Amplifier (OP-Amplifier) applications Zero crossing amplifier.	12	

Ministry of higher Education & Scientific research

Erbil Polytechnic University

Erbil Technology Collage

Department: AIT

Final 3rd Semester/ Annual Exam.
1st Trial

Stage: Second

Subject: Power Electronic

Code: POE303

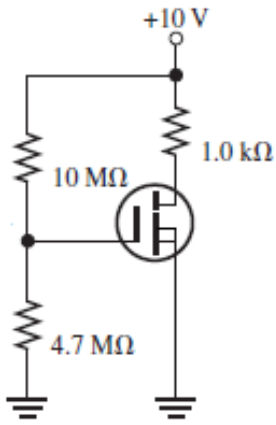
Time: Two Hours

Date: 20 /12 /2022

Q1/ Answer **ONLY FIVE** of the statements below: **(20 Marks)**

1- The phase displacement of three phase full wave bridge rectifier circuit is --------
2- Draw the equivalent circuit of UJT.
3- If $V_{GS} > V_{GS\,(off)}$ of N-channel JFET, what is I_D?
4- Show the difference between DMOSFET and EMOSFET in channels, biasing (operation) and type of switching.
5- What are the operation regions of BJT, in which region BJT works as an amplifier?
6- Show the effect of temperature on the drain curve of IGBT by drawing.
7- By relation show the condition that makes BJT saturated in circuit.

Q2/A/ In circuit below: **(15 Marks)**

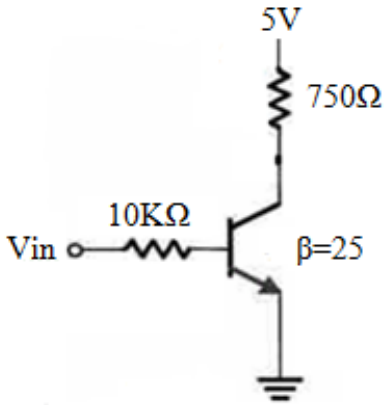


a- What is the type of this transistor?
b- If $V_{GS\,(th)}$ =2V and $I_{D\,(on)}$ =3mA at V_{GS} = 4V. Calculate I_D.
c- Determine V_{GS} and $V_{DS.}$

B/ For a UJT the **η** is determined to be 0.6. The interbase resistance $\mathbf{r_{BB}}$ indicates 7KΩ.what is the value of $\mathbf{r_{B1}}$ and $\mathbf{r_{B2}}$? **(10Marks)**

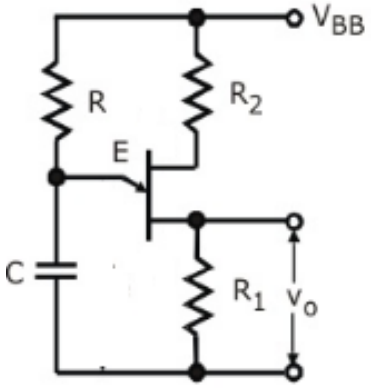
Q3/A/ In circuit below: **(15Marks)**

Calculate the minimum input voltage **(V_{BB})** that makes the BJT is saturated if **V_{CE}=0.4V.**



B/ Determine a value of R that ensure proper turn ON and turn OFF of the UJT.

(η=0.5, V_V=1V, I_V=10mA, I_P=20µA, V_{BB}=15V and V_P=14V). **(10 Marks)**



Q4/A/ A three phase half wave rectifier, supplied with (V_S = 24sin 628 t) and connected to a resitive load (R_L = 1KΩ), answer the following: **(15 Marks)**

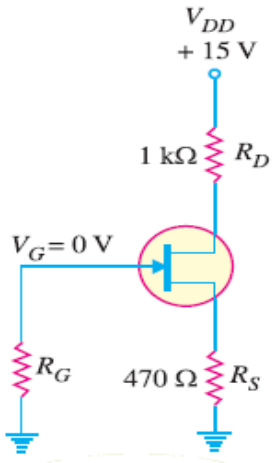
1-Draw this circuit.

2-Sketch the input and output voltage waveforms.

3-Find (V_{DC}), (Vrms), (I_{DC}), (Irms).

4-Calculate (efficiency η).

B/ Find V_{GS} and V_{DS} given that I_D = 5 mA for the circuit below: **(15 Marks)**



Lecturer

Prof. Dr. Farah Amer